Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

1. (Currently amended) A radio transmission power control circuit for controlling transmission power of a half-duplex radio transceiver, comprising:

a radio frequency (rf) downconverter that produces a downconverter output having a frequency equal to the frequency difference between a first downconverter input based on a transmitted signal of a radio transmitter of the half-duplex radio transceiver and a second downconverter input based on a local oscillator signal;

a receiver baseband circuit of [[a]] the half-duplex radio transceiver that alternately transmits and receives radio signals, the receiver baseband circuit operating ~~when~~ while the half-duplex radio transceiver is receiving, to ~~to~~ process received radio signals, and while ~~and when~~ the half-duplex radio transceiver is transmitting, to ~~to~~ process the downconverter output to produce a power signal representative of the power of the transmitted signal; and

a feedback control circuit that produces a transmitter gain control signal to control the transmitted signal power so as to minimize the difference between the power signal and a power reference signal representative of the desired power of the transmitted signal.

2. (Previously presented) A circuit according to claim 1, wherein the local oscillator signal is used by the radio transmitter such that the transmitted signal has a frequency determined by the local oscillator signal.

3. (Original) A circuit according to claim 1, further comprising:

an analog-to-digital converter that converts the power signal to a representative digital power signal; and

wherein the feedback control circuit produces the transmitter gain control signal so as to minimize the difference between the digital power signal and the power reference signal.

4. (Original) A circuit according to claim 1, wherein the first downconverter input is developed by a directional coupler that senses the transmitted signal.

5. (Original) A circuit according to claim 1, wherein the radio transmitter is part of a wireless local area network transceiver.

6. (Original) A circuit according to claim 1, wherein the radio transmitter is part of a time division duplex system.

7. (Currently Amended) A method of controlling radio transmission power of a half-duplex radio transceiver, the method comprising:

producing with a radio frequency (rf) downconverter a downconverter output having a frequency equal to the frequency difference between a first downconverter input based on a transmitted signal of a radio transmitter of the half-duplex radio transceiver and a second downconverter input based on a local oscillator signal;

processing the downconverter output with a receiver baseband circuit of [[a]] the half-duplex radio transceiver that alternately transmits and receives radio signals, the receiver baseband circuit operating while ~~when~~ the half-duplex radio transceiver is receiving, to ~~to~~ process received radio signals, and while ~~and when~~ the half-duplex radio transceiver is transmitting, to ~~to~~ process a power signal representative of the power of the transmitted signal; and

producing a transmitter gain control signal to control the transmitted signal power so as to minimize the difference between the power signal and a power reference signal representative of the desired power of the transmitted signal.

8. (Previously presented) A method according to claim 7, wherein the local oscillator signal is used by the radio transmitter such that the transmitted signal has a frequency determined by the local oscillator signal.

9. (Original) A method according to claim 7, further comprising:

converting the power signal to a representative digital power signal; and

wherein the transmitter gain control signal is produced so as to minimize the difference between the digital power signal and the power reference signal.

10. (Original) A method according to claim 7, wherein the first downconverter input is developed by a directional coupler that senses the transmitted signal.

11. (Original) A method according to claim 7, wherein the radio transmitter is part of a wireless local area network transceiver.

12. (Original) A method according to claim 7, wherein the radio transmitter is part of a time division duplex system.

13. (Currently amended) A radio transmission power control circuit for controlling transmission power of a half-duplex radio transceiver, comprising:

a radio frequency (rf) quadrature downconverter that produces a quadrature downconverter output having a frequency equal to the frequency difference between a first quadrature downconverter input based on a transmitted signal of a radio transmitter of the half-duplex radio transceiver and a second quadrature downconverter input based on a local oscillator signal;

a receiver baseband circuit of [[a]] the half-duplex radio transceiver that alternately transmits and receives radio signals, the receiver baseband circuit operating while ~~when~~ the half-duplex radio transceiver is receiving, to ~~to~~ process received radio signals, and while ~~and when~~ the half-duplex radio transceiver is transmitting, to ~~to~~ process the quadrature downconverter output to produce a power signal representative of the power of the transmitted signal; and

a feedback control circuit that produces a transmitter gain control signal to control the transmitted signal power so as to minimize the difference between the power signal and a power reference signal representative of the desired power of the transmitted signal.

14. (Previously presented) A circuit according to claim 13, wherein the local oscillator signal is used by the radio transmitter such that the transmitted signal has a frequency determined by the local oscillator signal.

15. (Previously presented) A circuit according to claim 13, further comprising:

an analog-to-digital converter that converts the power signal to a representative digital power signal; and

wherein the feedback control circuit produces the transmitter gain control signal so as to minimize the difference between the digital power signal and the power reference signal.

16. (Previously presented) A circuit according to claim 13, wherein the first quadrature downconverter input is developed by a directional coupler that senses the transmitted signal.

17. (Previously presented) A circuit according to claim 13, wherein the radio transmitter is part of a wireless local area network transceiver.

18. (Previously presented) A circuit according to claim 13, wherein the radio transmitter is part of a time division duplex system.

19. (Currently amended) A method of controlling radio transmission power of a half-duplex radio transceiver, the method comprising:

producing with a radio frequency (rf) quadrature downconverter a quadrature downconverter output having a frequency equal to the frequency difference between a first quadrature downconverter input based on a transmitted signal of a radio transmitter of the half-duplex radio transceiver and a second quadrature downconverter input based on a local oscillator signal;

processing the quadrature downconverter output with a receiver baseband circuit of [[a]] the half-duplex radio transceiver that alternately transmits and receives radio signals, the receiver baseband circuit operating while ~~when~~ the half-duplex radio transceiver is receiving, to ~~to~~ process received radio signals, and while ~~and when~~ the half-duplex radio transceiver is

transmitting, to ~~to~~ process a power signal representative of the power of the transmitted signal; and

producing a transmitter gain control signal to control the transmitted signal power so as to minimize the difference between the power signal and a power reference signal representative of the desired power of the transmitted signal.

20. (Previously presented) A method according to claim 19, wherein the local oscillator signal is used by the radio transmitter such that the transmitted signal has a frequency determined by the local oscillator signal.

21. (Previously presented) A method according to claim 19, further comprising:

converting the power signal to a representative digital power signal; and

wherein the transmitter gain control signal is produced so as to minimize the difference between the digital power signal and the power reference signal.

22. (Previously presented) A method according to claim 19, wherein the first quadrature downconverter input is developed by a directional coupler that senses the transmitted signal.

23. (Previously presented) A method according to claim 19, wherein the radio transmitter is part of a wireless local area network transceiver.

24. (Previously presented) A method according to claim 19, wherein the radio transmitter is part of a time division duplex system.